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D. Davis
1-31-02
P.C. Davis 23



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Paul C. Davis
Case: 23
Serial No.: To Be Assigned
Filing Date: July 23, 2001
Title: Mixed Signal Integrated Circuit
With Improved Isolation
Group: To Be Assigned
Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

U.S. Patent Documents

1. U.S. Patent Application Serial No. 09/631,862, filed August 3, 2000 in the name of inventors Kuhn et al. and entitled "Improved Substrate Isolation for Analog/Digital IC Chips."
2. U.S. Patent No. 5,999,440 issued on 12/07/99 to Crafts
3. U.S. Patent No. 5,727,180 issued on 3/10/98 to Davis et al.
4. U.S. Patent No. 5,116,777 issued on 5/26/92 to Chan et al.

Other Documents

1. Jerry Twomey, "Designing Analog and Mixed-Signal Circuits on Digital-CMOS Processes," DesignFeature, SOC Design, EDN, pp. 109-116, August 3, 2000.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

A handwritten signature in black ink, reading "Wayne L. Ellenbogen", followed by a long horizontal flourish line extending to the right.

Date: July 23, 2001

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